

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

M.Tech II Semester (R25) First Mid Term Examinations

TIME TABLE

Timings: 10.00 a.m. to 12.00 Noon

8th July 2026

Date	Power Electronics & Electric Drives	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning)
13.07.2026 (Monday)	Switched Mode Power Conversion (D252AN01)	Digital CMOS Circuit Design (D2527701)	Machine Learning (D252AI01)
14.07.2026 (Tuesday)	Power Electronic Control of Electrical Drives (D252AN02)	Architectures for DSP (D2527702)	Deep Learning (D252AI02)
15.07.2026 (Wednesday)	Digital Controller for Power Electronic Applications (D252AN03)	Embedded Real Time Operating Systems (D2527703)	Advanced Data Structures and Algorithms (D252AI03)
16.07.2026 (Thursday)	Control and Integration of Renewable Energy Systems (D252ANA0)	VLSI Testing and Testability (D25277A2)	Generative AI (D252AIA3)
17.07.2026 (Friday)	Industrial Internet of Things (D25277B1)	Internet of Things (D25277B3)	Soft computing (D252AIB3)

Note: Seating arrangement will be notified before the commencement of the examination by the respective departments.


Vice Principal
(Academics)

Copy to: HODs – EEE/ECE/CSE/CoE

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)